

1/8

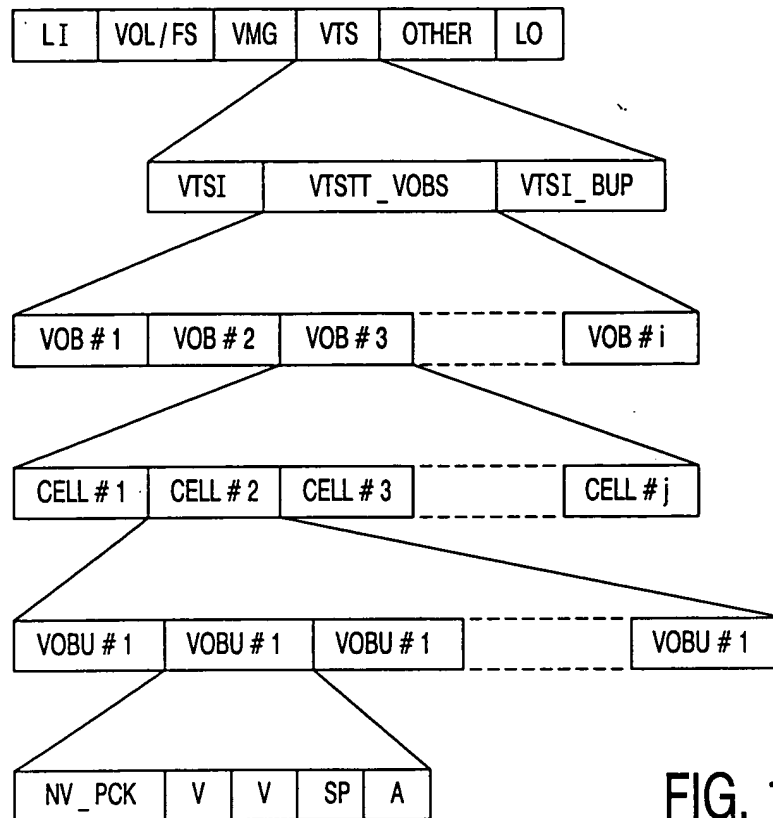


FIG. 1

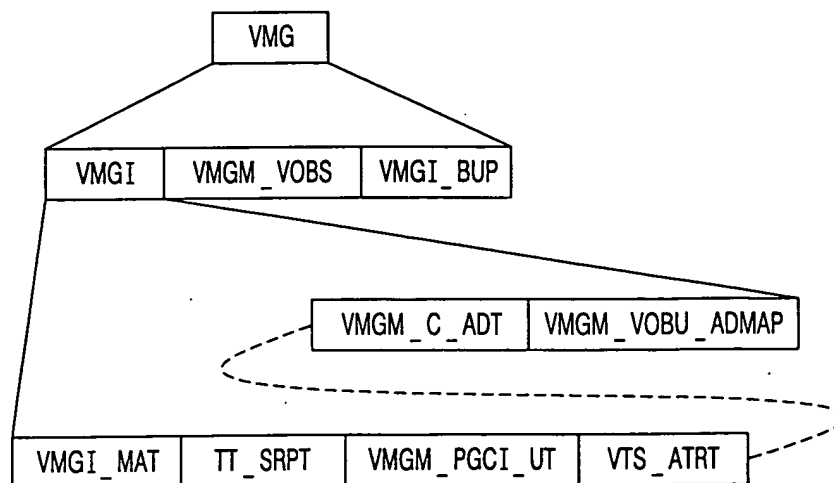


FIG. 2

2/8

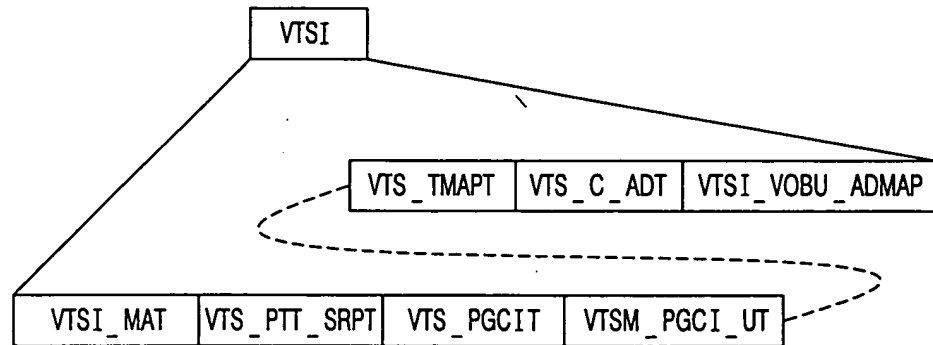


FIG. 3

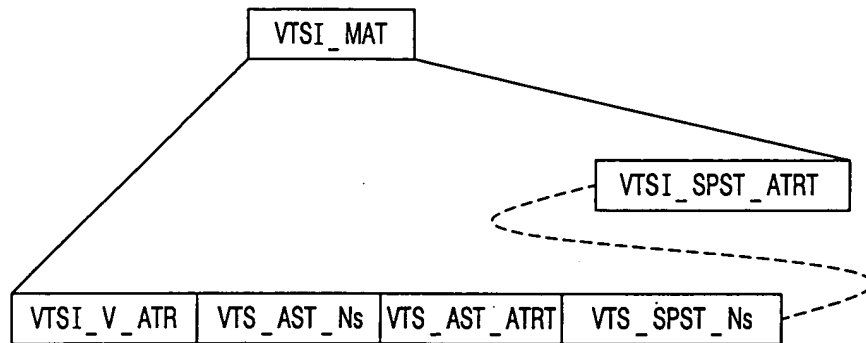


FIG. 4

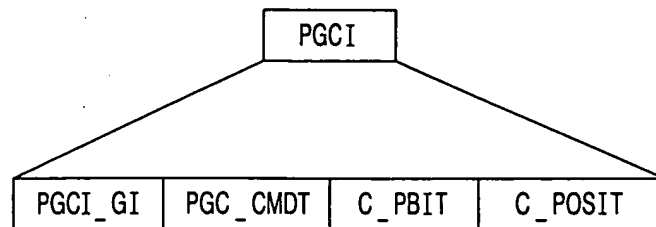


FIG. 5

3/8

VMGI/TT_SRPT/TT_SRP's				VTSI/ VTS_PTT_S
EXAMPLE	TT_PB_TY	PTT_Ns	VTS_TTN	
PLAY LISTS	PLAY LIST 1	0000 01x1	1	①
	PLAY LIST 2		2	
			..	
	AS ORIGINAL I	0000 0101	4	②
			..	
	PLAY LIST K	0000 0101	3	③
			..	
	FREE SPACE	0000 0111	1	④
			..	
	PLAY LIST N	0001 01x1	N (< 50)	⑤
ORIGINALS	ORIGINAL 1	0000 01x1	N + 1	
	ORIGINAL 2		N + 2	⑥
			..	
	ORIGINAL I	0000 0101	4	
			..	⑦
	ORIGINAL K	0000 0101	5	
			..	⑧
	FREE SPACE	0000 0111	1	
			..	⑨
	ORIGINAL N	0000 01x1	N + N	

AGL_Ns == '1'

TT_PTL_ID_FLD == zero

VTSN == '1'

Bit 1 of TT_PB_TY indicates free space

FIG. 6A

4/8

VTSI/VTS_PTT_SRPT/TTUs	
PGCN	PGN
1	1
	..
2	1
..	..
I	1
	2
	3
	4
K	1
	2
	3
M	1
N	1
N	..
N + 1	1
	..
N + 2	1
..	..
N + I	1
	2
	3
	4
N + K	1
	2
	3
	4
N + M	1
N + N	1
	..

FIG. 6B

5/8

VTSI/VTS_PGCIT/VTS_PGC I_SRP	
VTS_TTN	VTS_PGC I_SA
1	
2	
..	
⑦ I	→ ORIGINAL I PGC I
⑧ ..	
K	→ PLAYLIST K PGC I
⑨ ..	
M	→ FREE SPACE M PGC I
..	
N	
N + 1	→ ORIGINAL 1 PGC I
N + 2	→ ORIGINAL 2 PGC I
..	
⑩ N + I	→ ORIGINAL I PGC I
..	
⑪ N + K	→ ORIGINAL K PGC I
..	
⑫ N + M	→ FREE SPACE M PGC I
..	
N + N	→ ORIGINAL N PGC I

Entry type == 1
 Block mode == 00b
 Block type == 00b
 PTL_ID_FLD == 0000h

Equal VTS_PGC I_SA indicate "no THL"

FIG. 6C

6/8

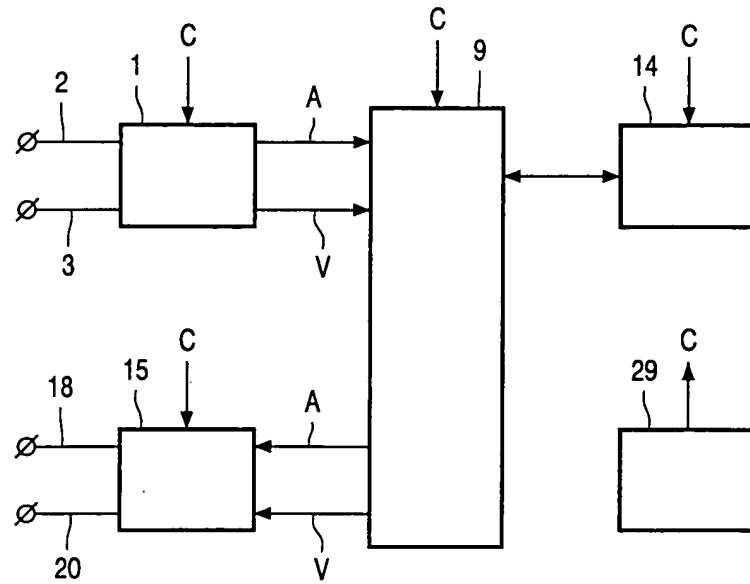


FIG. 7

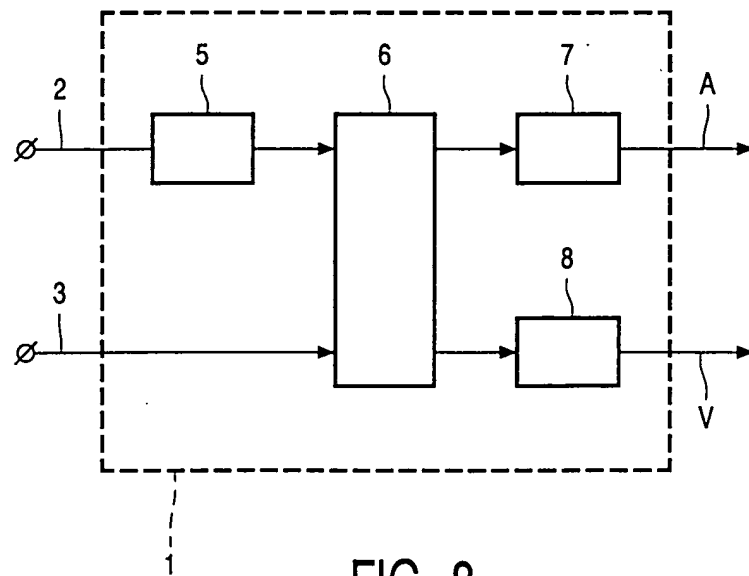


FIG. 8

7/8

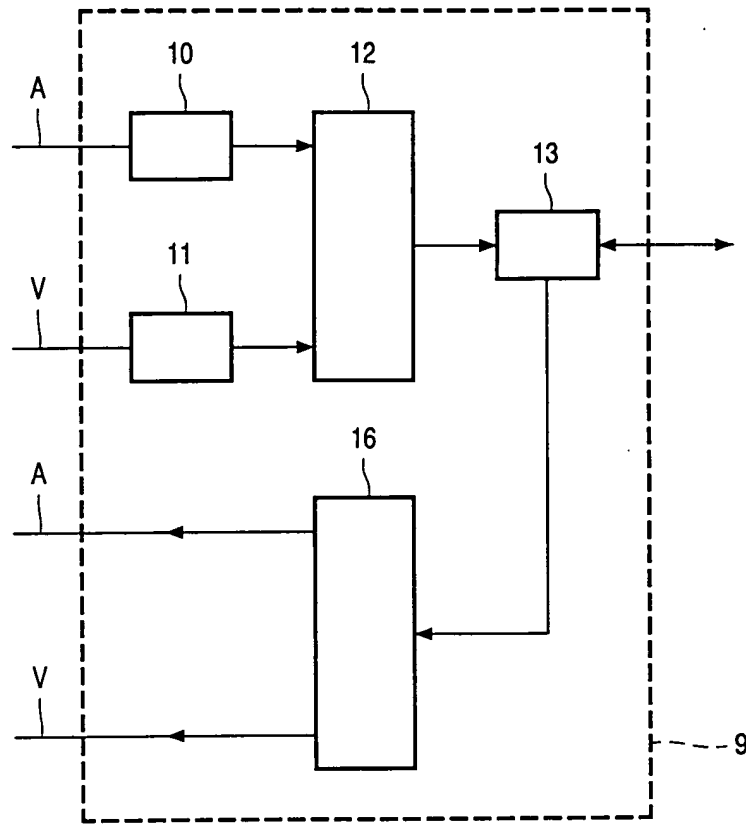


FIG. 9

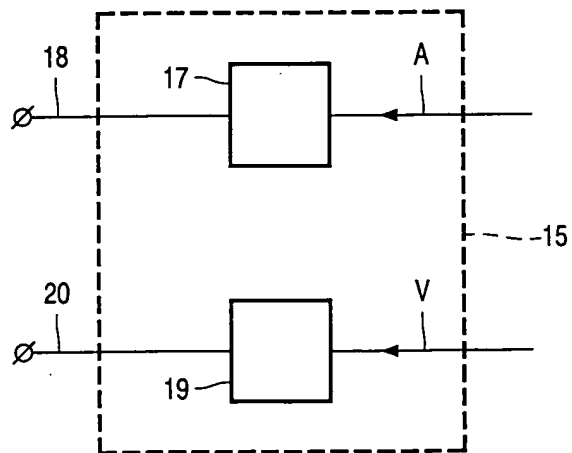


FIG. 10

FIG. 9

8/8

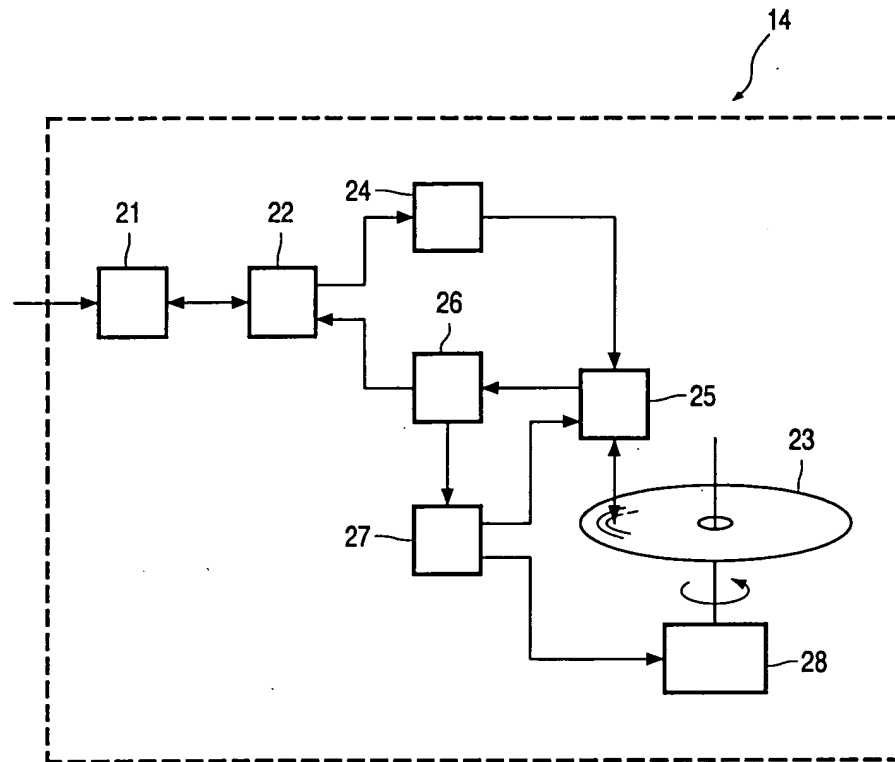


FIG. 11

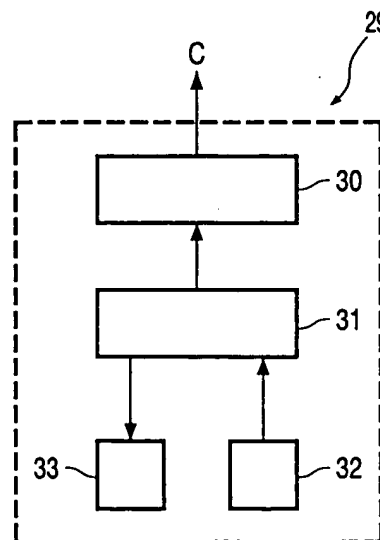


FIG. 12